

## ABSTRACT

A method and apparatus which provide one or more electromagnetic  
5 shield layers for integrated circuit chips containing electromagnetic circuit elements  
are disclosed. The shield layers may be in contact with the integrated circuit chip,  
including magnetic memory structures such as MRAMs, or in a flip-chip carrier, or  
both. A printed circuit board which supports the chip may also have one or more  
shield layers.

10003416.1 202004